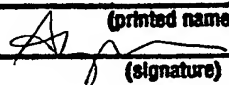


UNBLIND EQUALIZER ARCHITECTURE
FOR DIGITAL COMMUNICATION SYSTEMS

ABSTRACT OF THE DISCLOSURE

5 A method and apparatus for equalizing digital data signals is disclosed. The
method comprises the steps of demodulating and decoding an input signal having input
data to produce a data output, remodulating the data output to produce a pseudo-training
sequence including an idealized input signal, and generating equalizer parameters from
the pseudo-training sequence. The apparatus comprises a demodulator for demodulating
10 an input signal to produce a data output, a modulator, communicatively coupled to the
demodulator, for remodulating the data output to produce a pseudo-training sequence
including an idealized input signal, and a parameter generation module, communicatively
coupled to the modulator for generating equalizer parameters from the pseudo-training
sequence.

"Express Mail" mailing label number EV 329954758 US
Date of Deposit OCTOBER 22 2003
I hereby certify that this paper or fee is being de-
posited with the United States Postal Service "Express Mail
Post Office to Addressee" service under 37 CFR 1.10 on
the date indicated above and is addressed to:
Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.
SUZIE MCCLAVE

(printed name)


(signature)